



**DIONICS-USA
INCORPORATED**

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DIG-12-06-250M in ceramic package

Opto-Isolated Photovoltaic Iso-gate™ MOSFET Driver

Features:

- Completely Isolated Voltage Signal Generation
- Dielectrically Isolated
- Logic Circuit Compatibility
- High Open Circuit Voltage
- High Operating Temperature
- Fast Response Time
- High Isolation Resistance
- Excellent Input/Output Linearity
- Self Limiting Gate Voltage

Applications:

- Gate Drive For MOS devices
- Gate Drive For SCR
- Solid-State Relays
- Interface Between Logic Circuits & External Loads
- A.T.E. (Automatic Test Equipment)
- Switching Equipment
- Isolation Amplifiers
- Load Control From Microprocessor I/O Ports
- Thermocouple Open Detectors

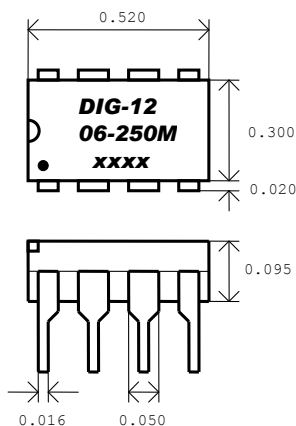
Description:

The photovoltaic MOSFET driver is a State-of- the-Art, optically coupled floating power source used primarily to control MOSFETs when electrical isolation between input and output is required. The ISO-GATE™ is a state-of-the-art opto-isolator incorporating DIONICS' photovoltaic (PV) diode arrays and an infrared LED. The diode array is a unique I.C. consisting of series-connected photovoltaic diodes. The diodes are completely isolated from each other and from their common substrate by means of SiO₂ Dielectric Isolation (DI). These photovoltaic output chips are electrically isolated but optically coupled to the LED.

The typical input circuit to the LED is a limiting resistor connected in series with the LED. When activated, the LED emits infrared light towards the photovoltaic diode array, which then responds by generating an open circuit voltage (Voc). This Voc is floating and is therefore completely isolated. The Voc value depends on the LED input drive and load impedance. DIONICS, Inc offers a wide variety of package styles including low-cost plastic Mini-DIP's, high reliability TO-5 metal cans, 8-pin ceramic side brazed DIP's and ceramic chip carriers for hybrid manufacturers. Other custom packages and custom designs are also available.

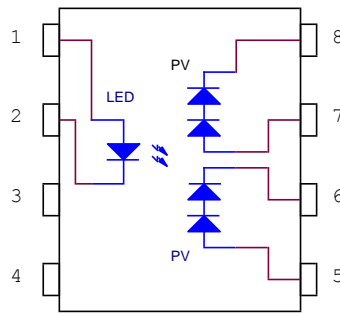
The DIG-12-06-250M has been 100% tested after a military burn-in.
Gull-wing surface mount versions are available.

❖ **Package Layout:**



Pin Designation	
Pin Number	DIG-12-06-250M
1	Input +
2	Input -
3	Not Connected
4	Not Connected
5	Output1+
6	Output1-
7	Output2+
8	Output2-

DIG-12-06-250M Equivalent Circuit



**DIG-12-06-250M
Ceramic Package Only**

❖ Absolute Maximum Ratings ($T_a = 25\text{ }^\circ\text{C}$)		
LED Forward Current	Steady State	100 mA
LED Forward Current	Peak 10% Duty Cycle	250 mA
LED Reverse Current	$V_r = 3\text{V}$	100 μA
LED Forward Voltage	$I_r = 20\text{mA}$	3.4V
LED Reverse Voltage		5V
Output Discharge Current		50mA
Operating Temperature Range	Side Braze D.I.P	-50 to 125 $^\circ\text{C}$
Storage Temperature		-50 to 125 $^\circ\text{C}$
Power Dissipation		250 mW

Individual Channel Electrical Characteristics ($T_a = 25\text{ }^\circ\text{C}$)

<i>Model Number</i>		<i>DIG-12-06-250M</i>		
Parameter & Test Condition	Symbol	Min.	Typ.	Unit
Open Circuit Voltage	V_{oc}			
$I_{led} = 2\text{mA}$		-	6.0	V
$I_{led} = 10\text{mA}$		6.5	7.3	V
$I_{led} = 30\text{ mA}; 50\% \text{ Duty Cycle}$		7.5	8.0	V
Short Circuit Current	I_{sc}			
$I_{led} = 2\text{mA}$		-	3.0	μA
$I_{led} = 10\text{mA}$		12.5	25.0	μA
$I_{led} = 30\text{ mA}; 50\% \text{ Duty Cycle}$		37.5	75.0	μA
Isolation Voltage (Ceramic)	V_{iso}	1000	-	VDC
Temperature Coefficients (channels in series)	θ_V	-	-80	mV / $^\circ\text{C}$
Turn-Off Time** $I_r = 50\text{mA}; C = 10\text{pF}; P.W = 100\mu\text{S}$ $f = 1\text{kHz}; R_s = 10\text{M}\Omega$	T_{off}	The Photovoltaic array stops generating current within one microsecond of the trailing edge of LED current. The discharge time is solely dependent upon discharge circuitry and capacitive load.		

** For proper turn-off operation, gate must be charged to 90% of its final value before turn-off initiated